

## **ABSTRACT**

**[66]** A reduced emission transmitter may include an encoder block, which may be partitioned into a first group comprising odd encoder processing cells and a second group comprising even encoder processing cells. A DAC block may also be partitioned into a first group comprising odd DAC processing cells and a second group comprising even DAC processing cells. The first group of encoder processing cells may be coupled to the first group of DAC processing cells and the second group of encoder processing cells may be coupled to the second group of DAC processing cells. The first group of encoder processing cells may be clocked using a first clock signal and the second group of encoder processing cells may be clocked using a second clock signal, the second clock signal being a delayed version of the first clock signal.